



6,825,095.B2.

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<b>TRANSMITTAL FORM</b>  (to be used for all correspondence after initial filing)	Application Number	09/955,632
	Filing Date	September 18, 2001
	First Named Inventor	Parekh et al.
	Art Unit	2813
	Examiner Name	Yennhu B. Huynh
Total Number of Pages in This Submission	Attorney Docket Number	MI22-1816

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	Patent No. 6,825,095B2 Issued: November 30, 2004	
<b>Certificate of Correction</b> AUG 31 2005		
<b>SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT</b>		
Firm or Individual	D. Brent Kenady, Reg. No. 40,045 Wells St. John, P.S.	
Signature		
Date	8-22-05	

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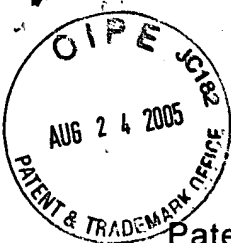
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AUG 31 2005



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. .... 6,825,095 B2  
Patent Issue Date ..... November 30, 2004  
Application Serial No. .... 09/955,632  
Filing Date ..... September 18, 2001  
Assignee ..... Micron Technology, Inc.  
Inventorship ..... Kunal R. Parekh et al.  
Attorney's Docket No. .... MI22-1816  
Title: Methods of Forming Capacitors

**REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT**  
**FOR PTO MISTAKE (37 C.F.R. 1.322(a))**

To: Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
ATTN: Decision and Certificate of Correction  
Branch of the Patent Issue Division  
  
From: D. Brent Kenady (Tel. 509-624-4276; Fax 509-838-3424)  
Wells St. John P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 6,825,095 B2, granted November 30, 2004, in accordance with the Certificate of Correction form attached hereto in duplicate.

The errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Since this Certificate of Correction is being requested due to PTO errors, it is believed that no fee is due. However, in the event that a fee is required for issuance of this Certificate of Correction, please charge the fee specified under


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37 C.F.R. § 1.20(a) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Attached hereto, in duplicate is Form PTO-1050, with at least one copy being suitable for printing.

Respectfully submitted,

Dated: 8-22-05

By:   
D. Brent Kenady  
Reg. No. 40,045

UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,825,095 B2  
DATED : November 30, 2004  
INVENTOR(S) : Parekh et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 1, line 28, please delete "a1" after "to".

Col. 2, line 27, please delete "circuitry" after "integrated" and insert --circuitry.--.

Col. 8, line 33, please delete "substrata"" after "semiconductive" and insert --substrate"--.

Page  
1 of 1

**Mailing Address of Sender:**  
D. Brent Kenady  
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601 West First Avenue, Suite 1300  
Spokane, WA 99201-3828

Patent No. 6,825,095 B2

UNITED STATES PATENT AND TRADEMARK OFFICE

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